

FIG. 1B
(Prior Art)

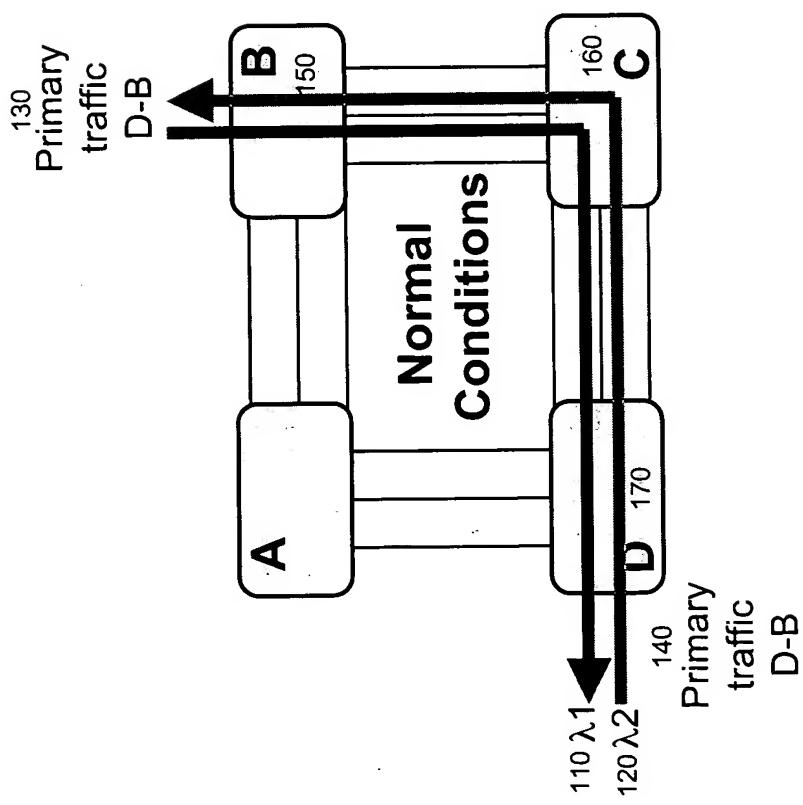


FIG. 1A
(Prior Art)

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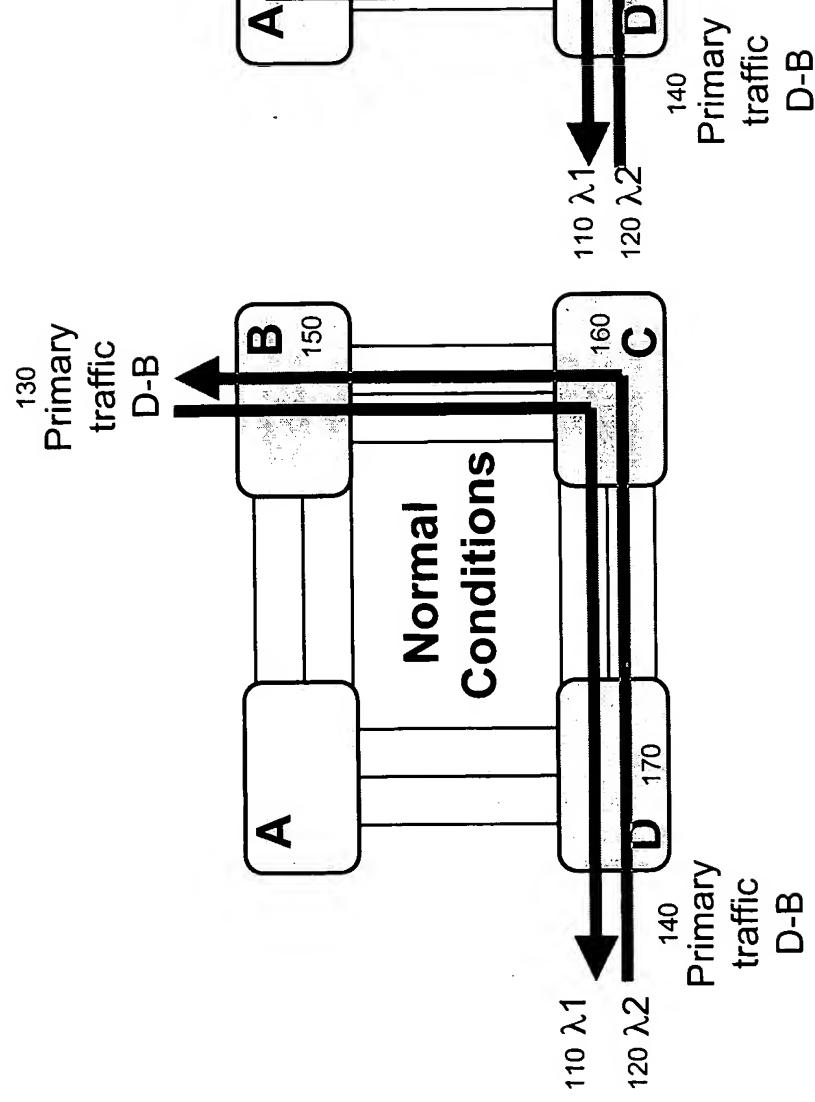


FIG. 2A
(Prior Art)

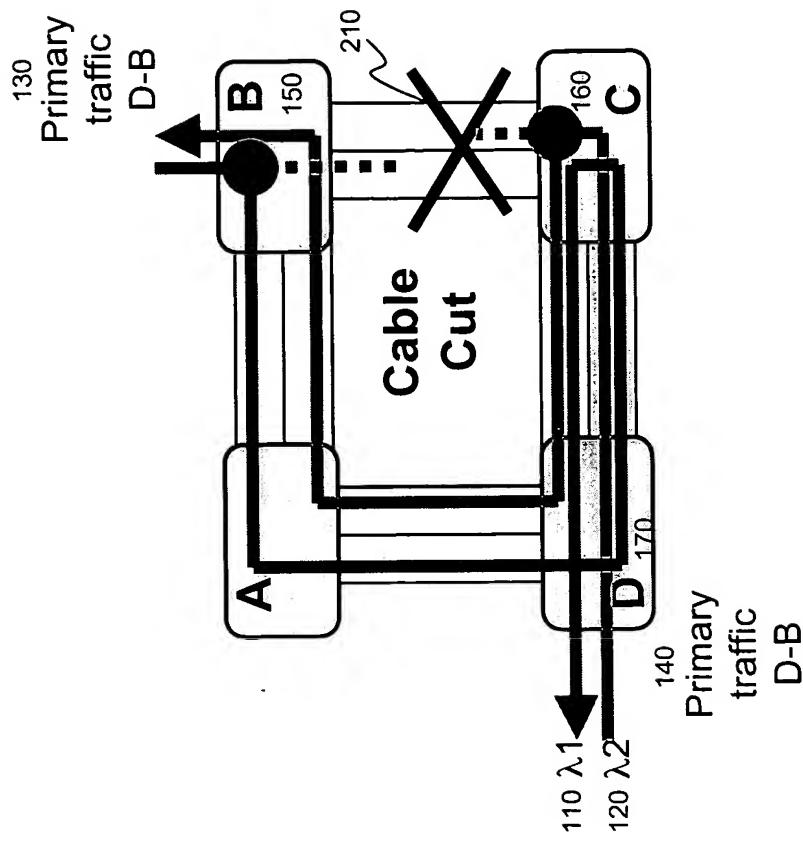
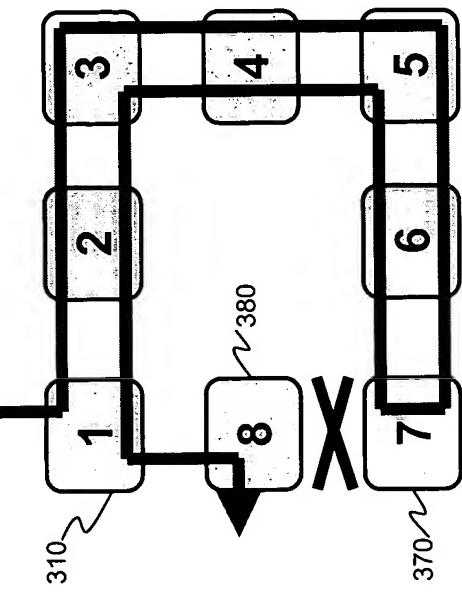


FIG. 2B
(Prior Art)

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**Protected channel with
loopback switching**



**Protected channel with
end-node switching**

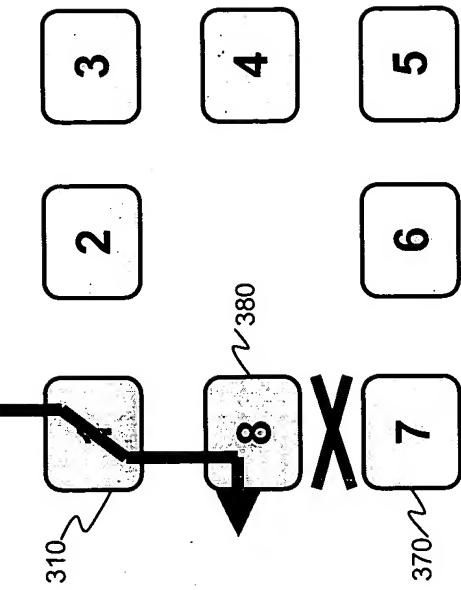


FIG. 3A
(Prior Art)

FIG. 3B
(Prior Art)

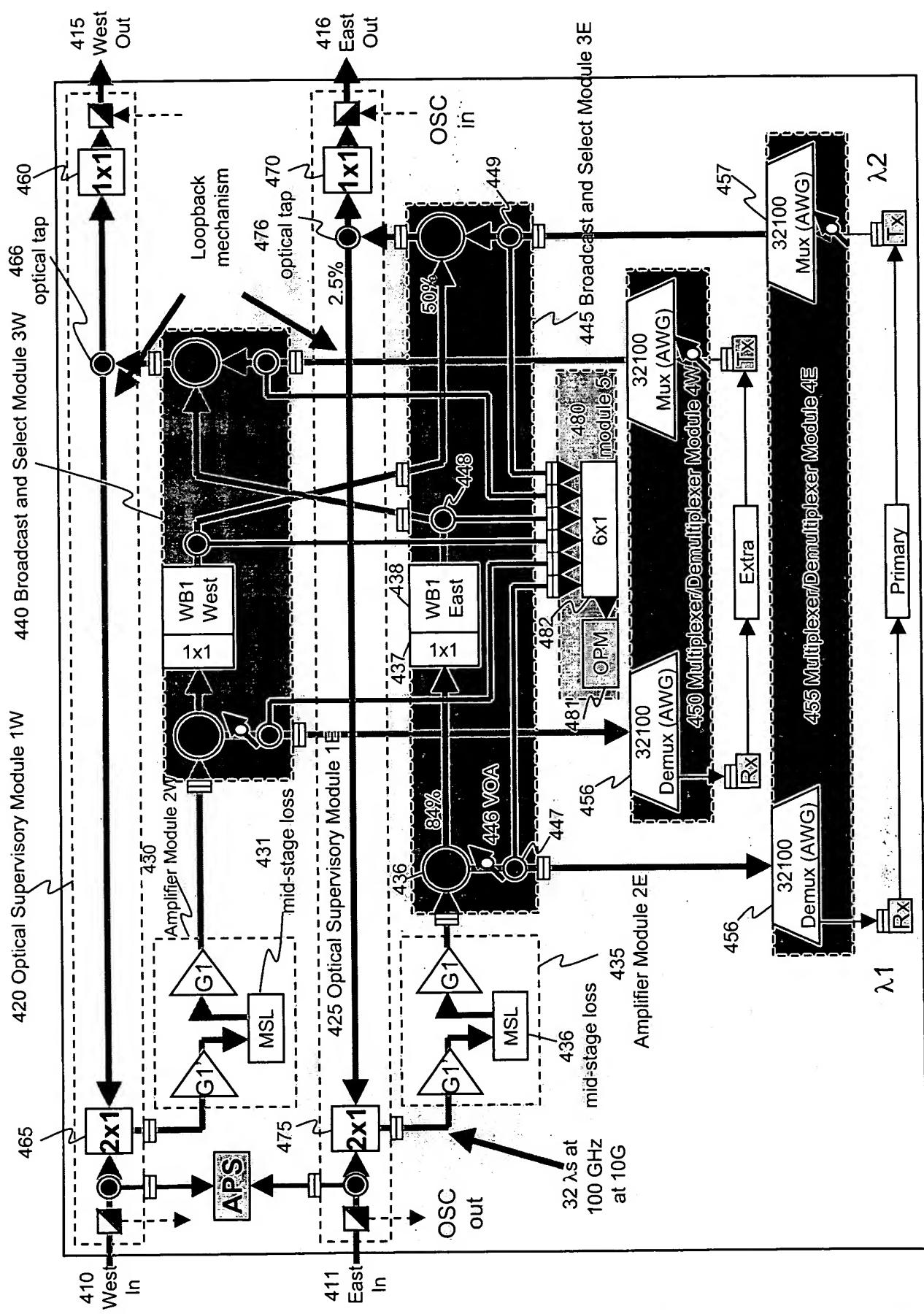


FIG. 4

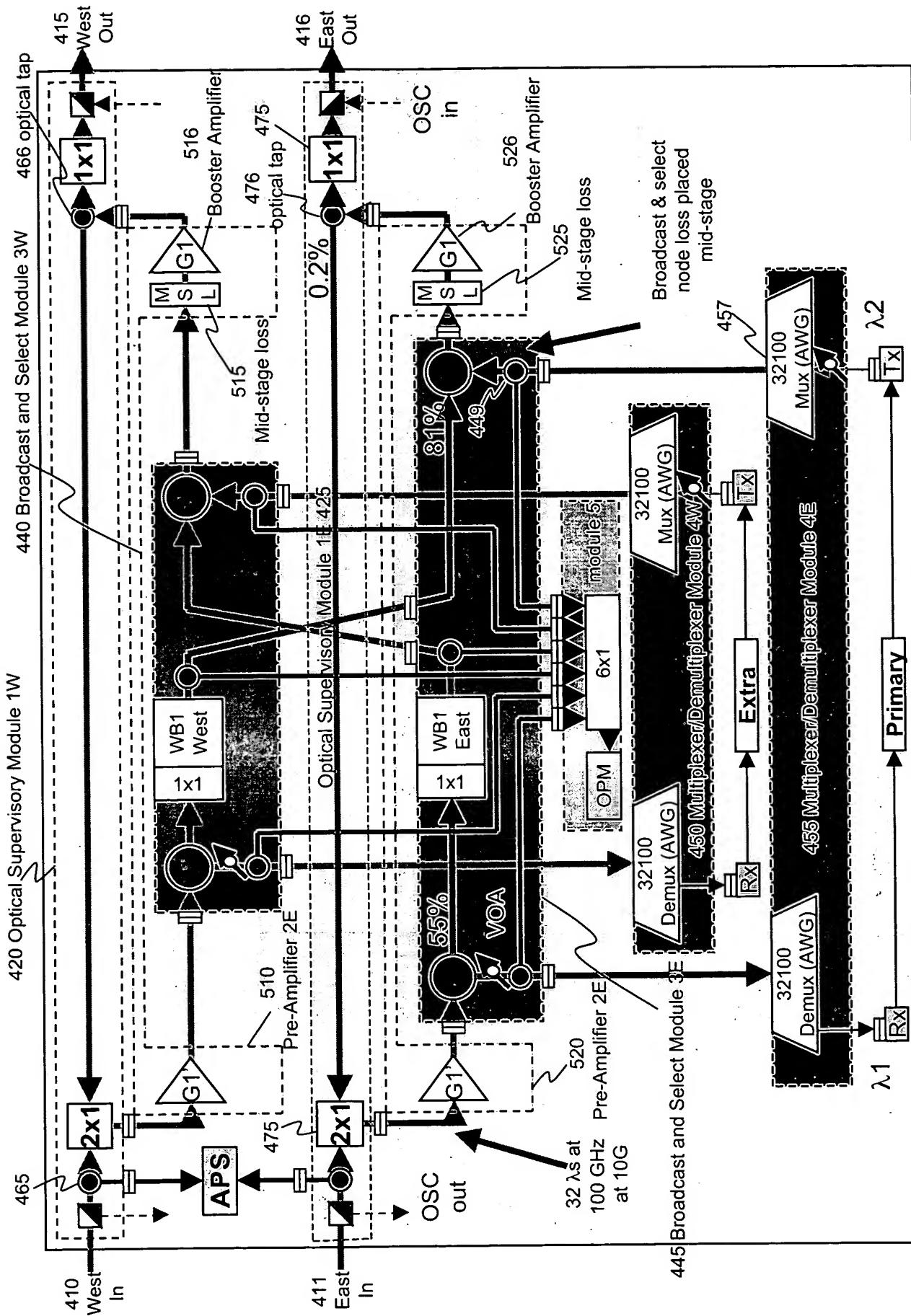
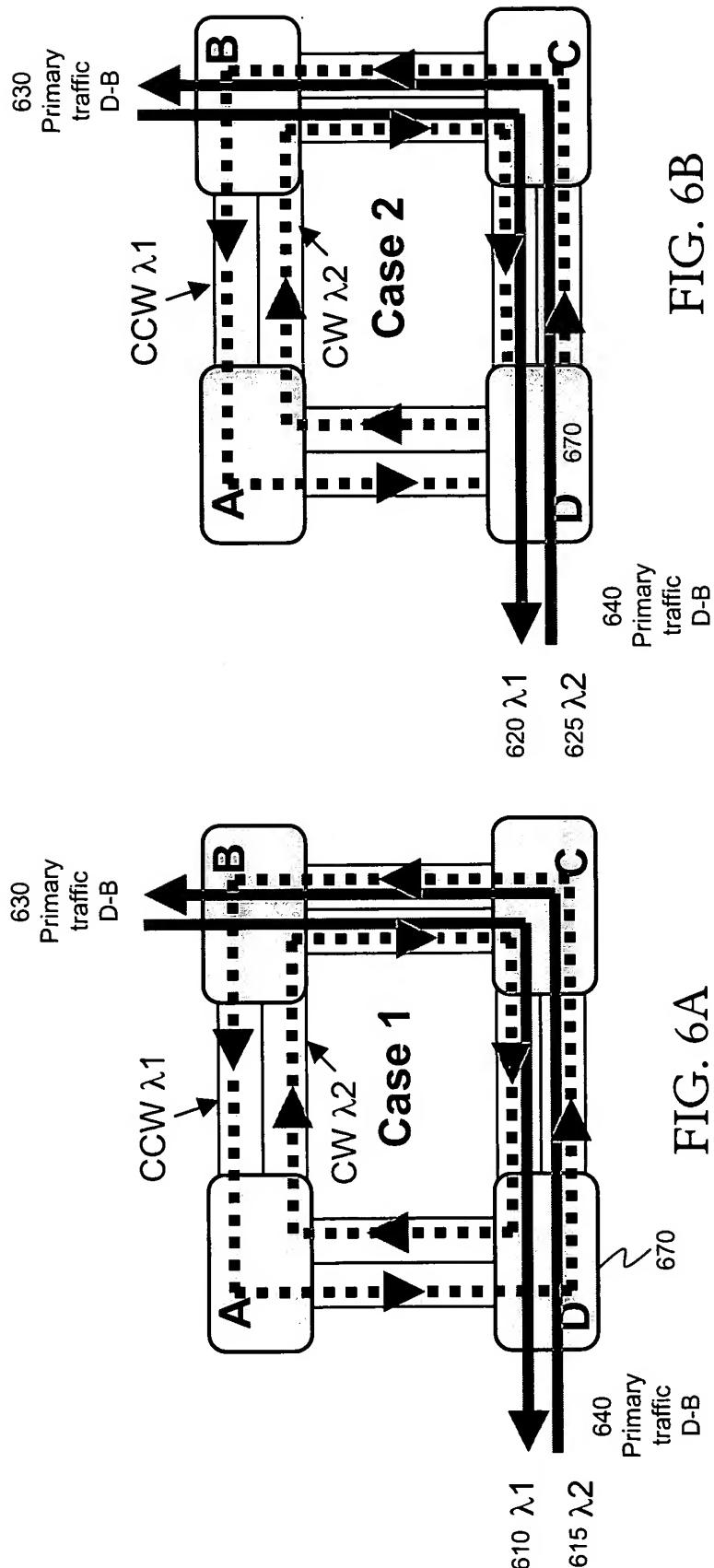
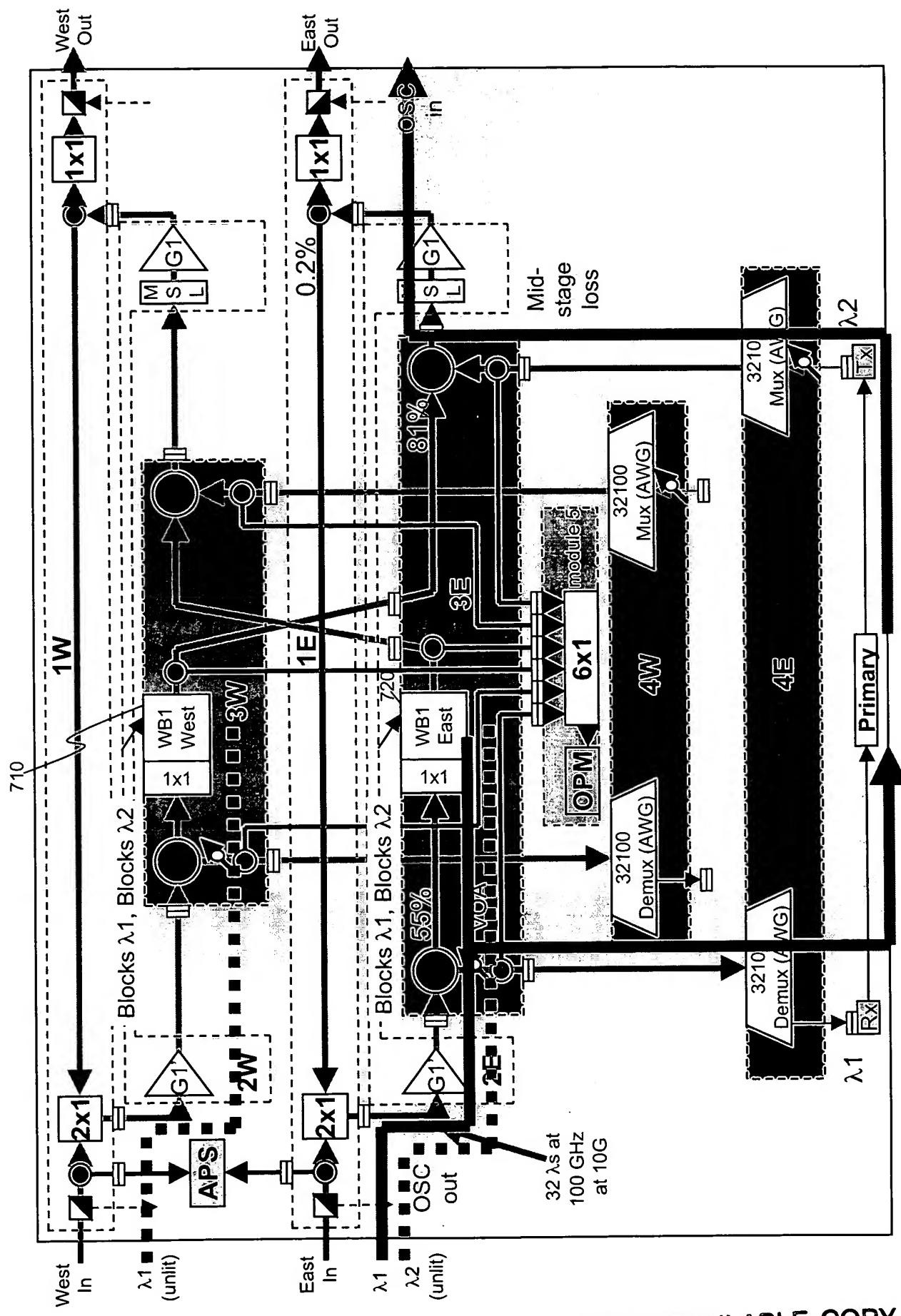


FIG. 5



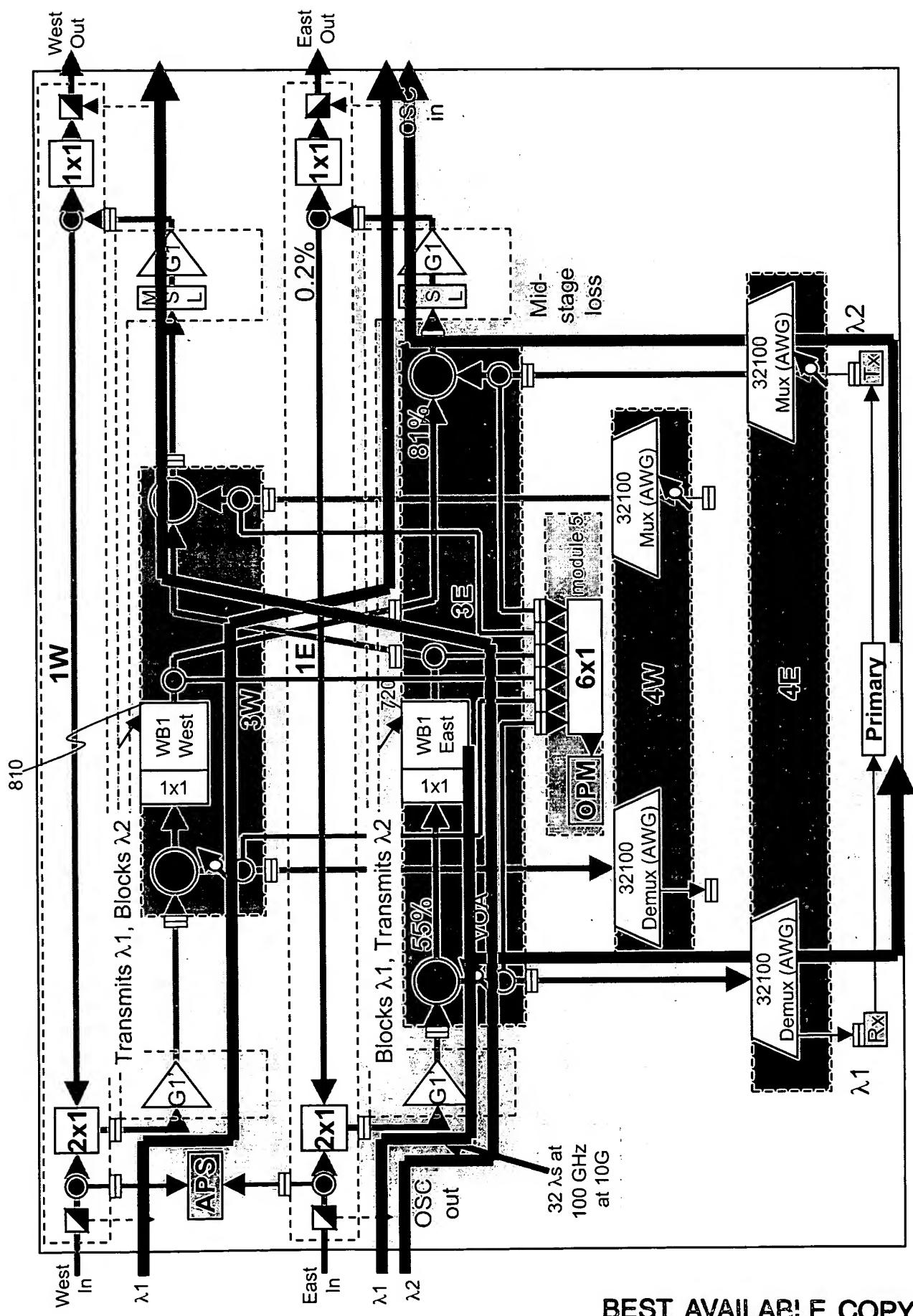
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FIG. 7



BEST AVAILABLE COPY

FIG. 8



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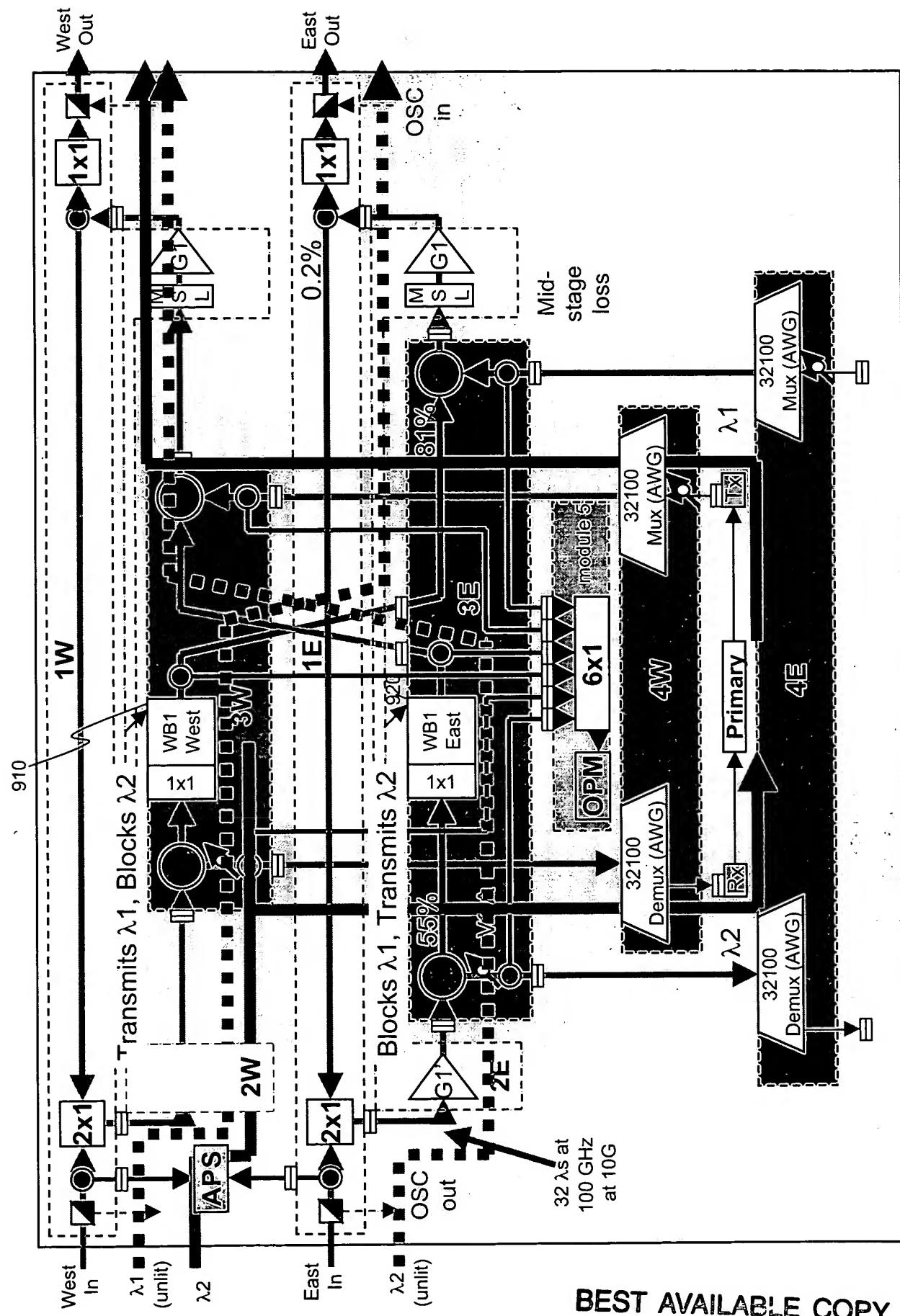
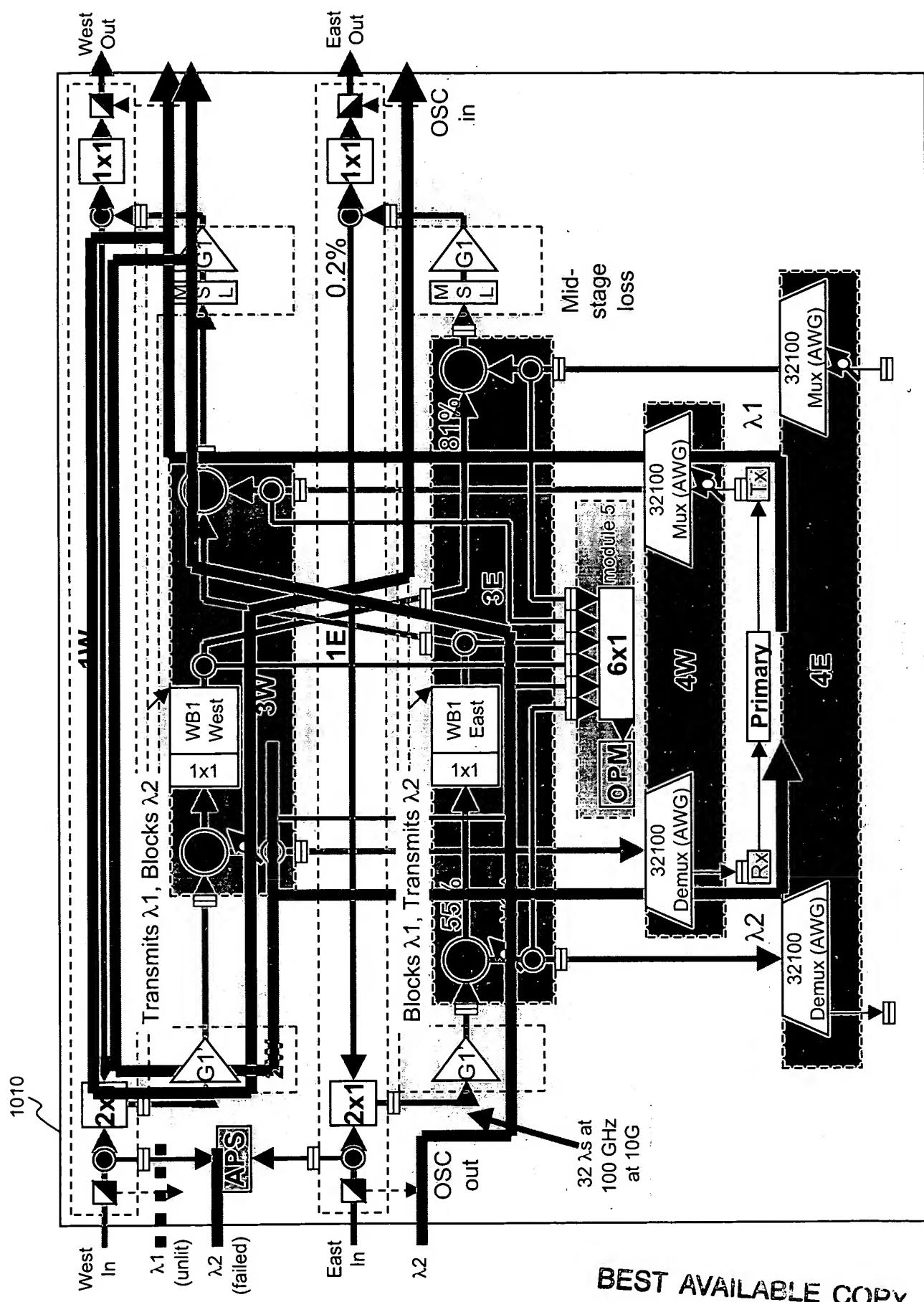


FIG. 9

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FIG. 10



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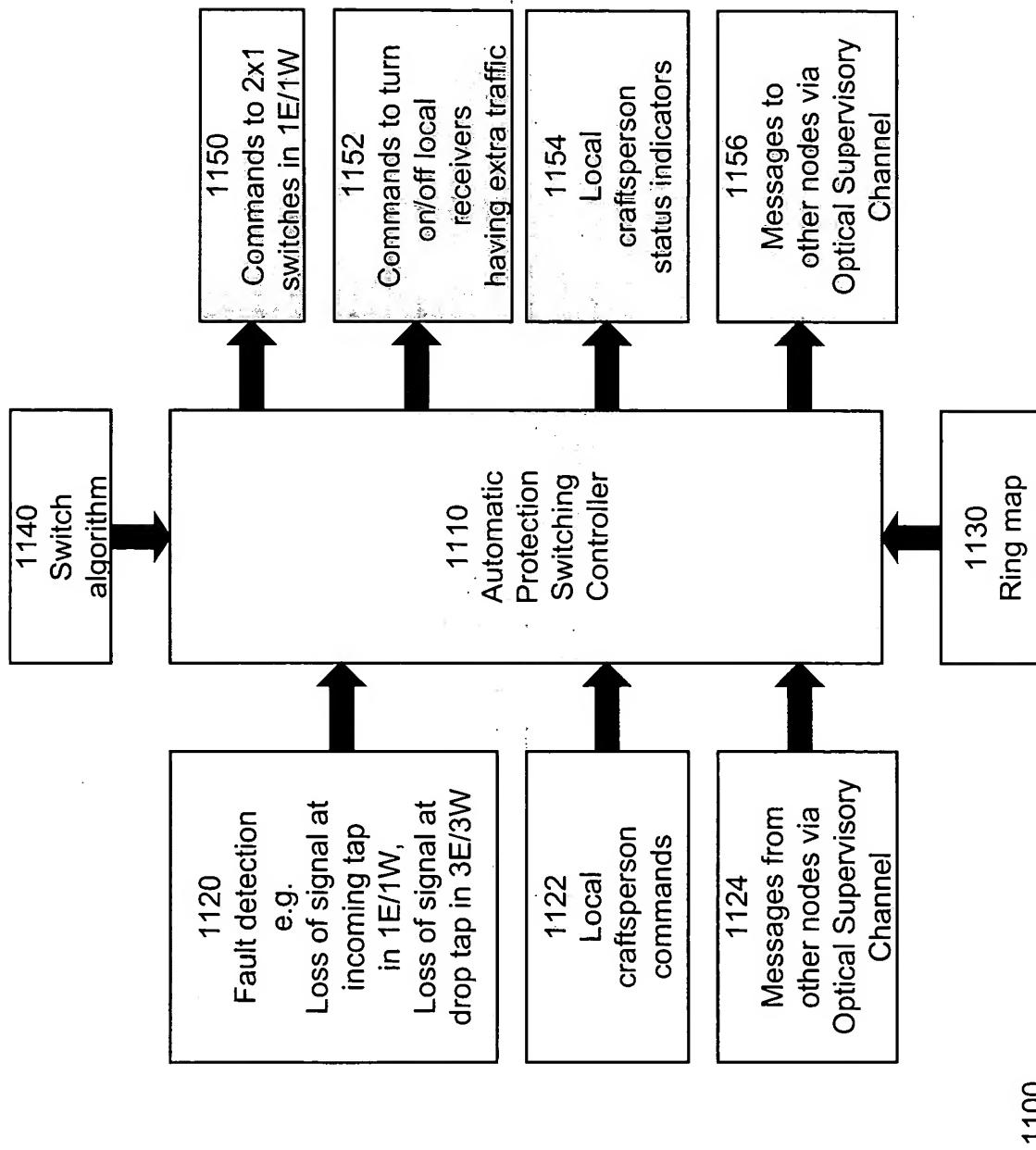


FIG. 11

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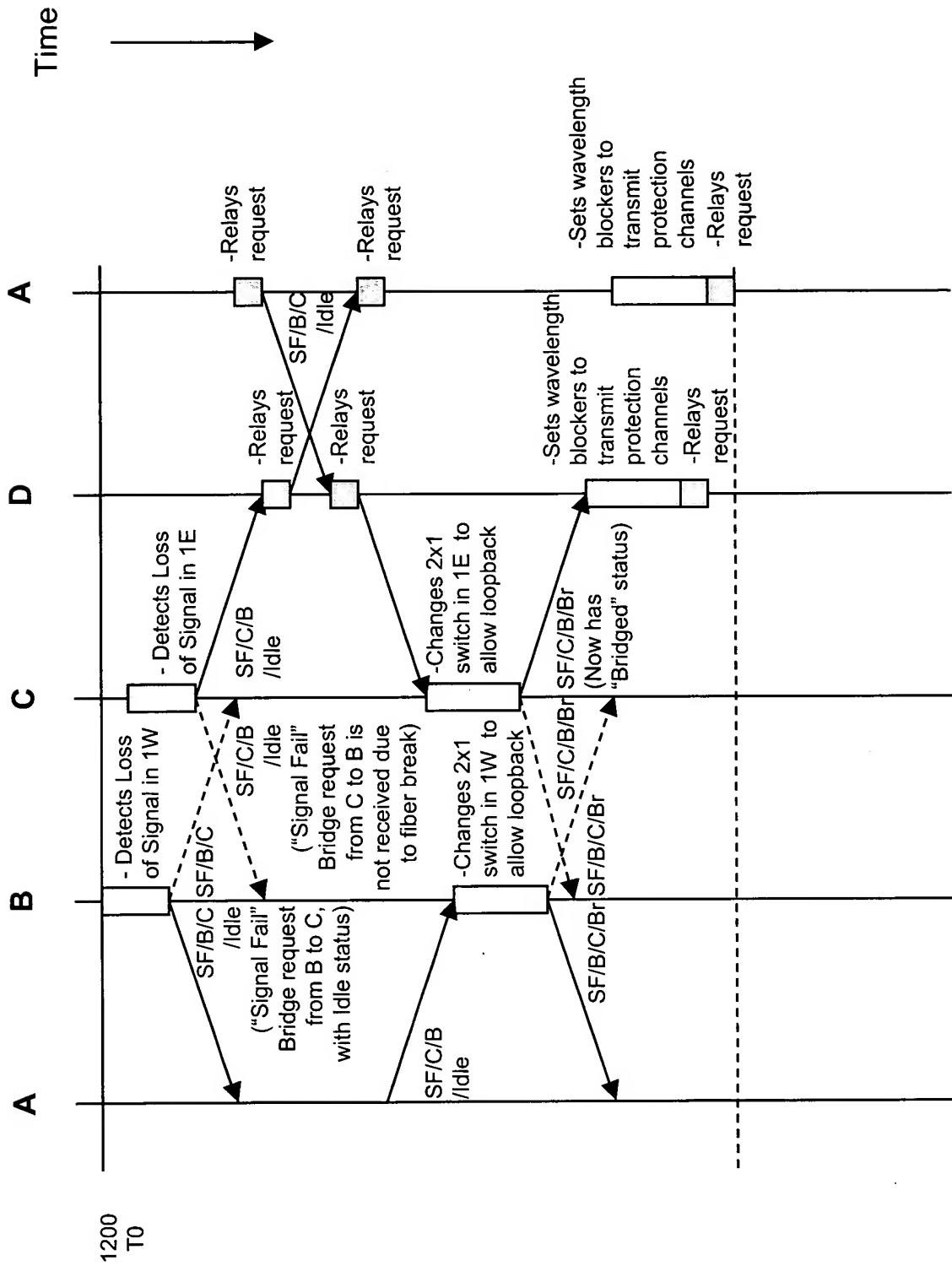


FIG. 12

FIG. 13

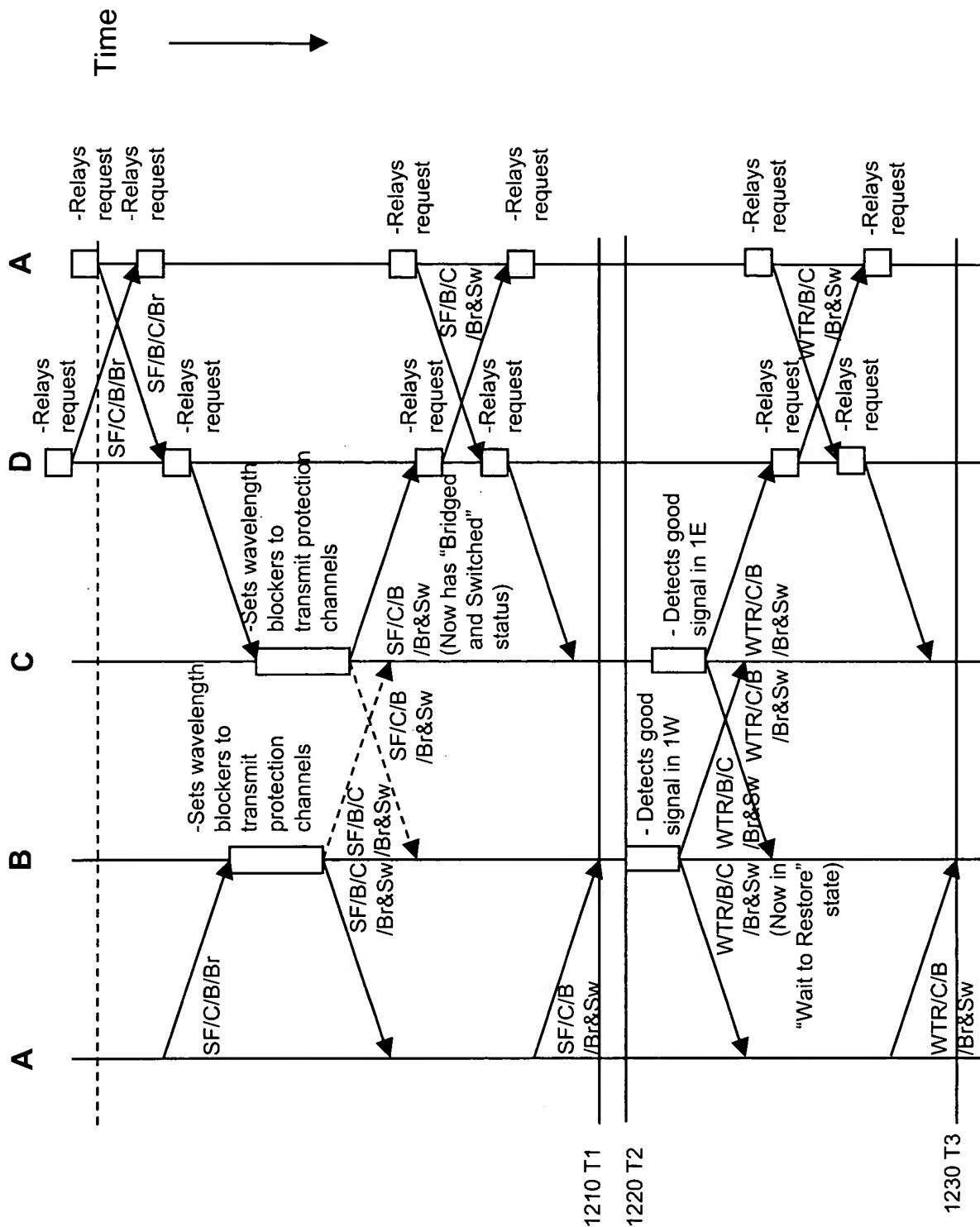


FIG. 14

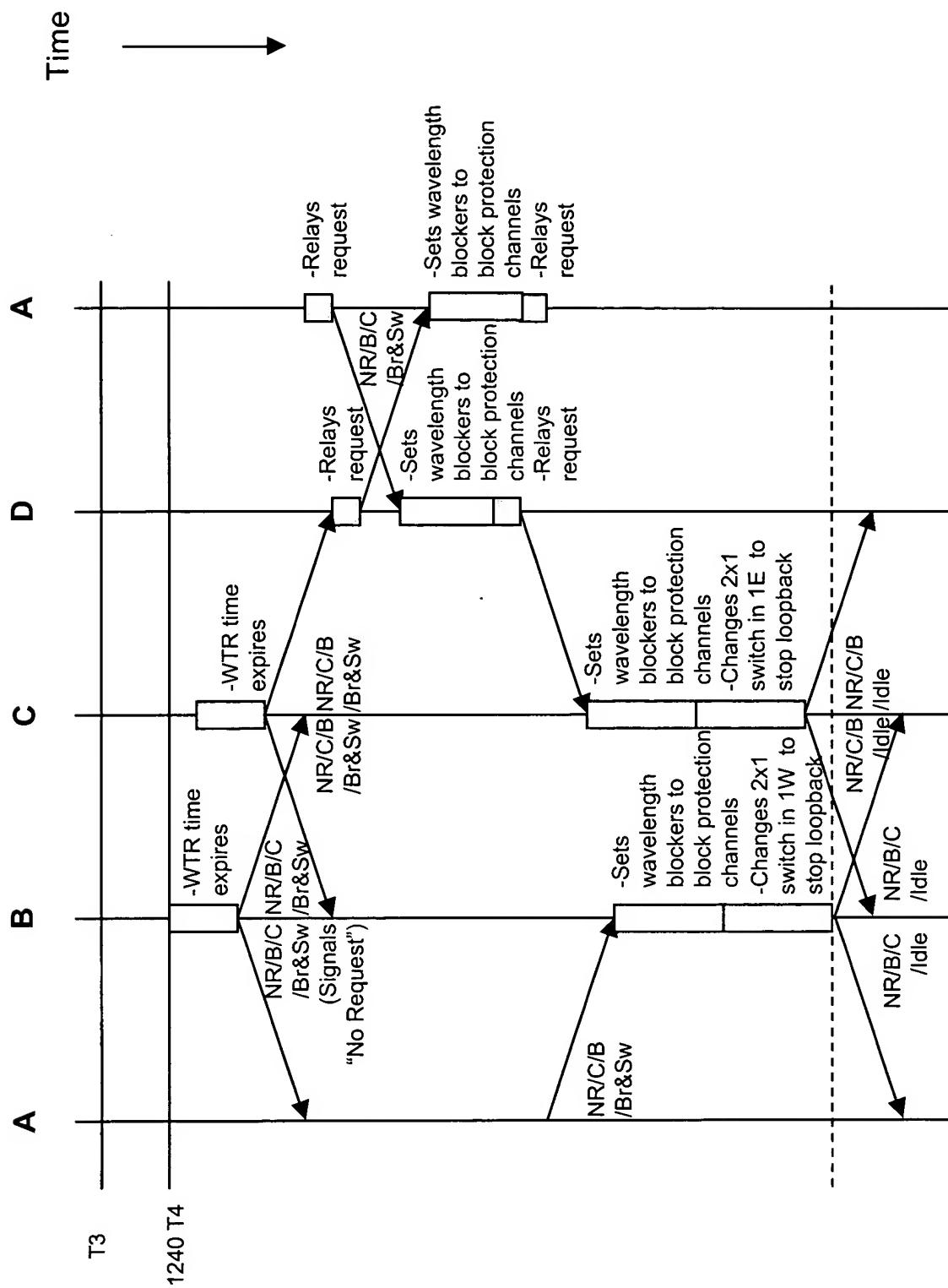


FIG. 15

